

ABSTRACT

The present invention describes aspects of adjusting a frequency range of a delay cell of a VCO. The aspects include providing a CMOS latch of predetermined gain, and altering
5 transconductance in the CMOS latch to alter a frequency range of the CMOS latch without altering the predetermined gain. The alteration of transconductance includes coupling at least one pair of series connected transistors in parallel with each switching device of the CMOS latch.